Linear Circuits

Component
École Nationale Supérieure d'Électrotechnique d'Électronique d'Informatique d'Hydraulique et des Télécommunications

Semester
Printemps

In brief

> **Ametys Code:** N8AE01A

> **Open to exchange students:** No

Presentation

Objectives

To master the concepts related to linear analog circuits based on bipolar transistors and MOSFETs.

Description

This course consists of four distinct lessons covering: the cascoded common-emitter configuration, the linear regulator, class A, B, and AB amplifiers, and the silicon MOSFET transistor. Each topic is illustrated with simulations using PSpice, allowing students to grasp the concepts covered in the lectures.

Pre-requisites

Fundamentals of transistor circuits (covered in the third year of undergraduate studies).