

SYSTÈME NUMÉRIQUE DE TRAITEMENT DE L'INFORMATION

 ECTS
5 credits

 Component
École Nationale
Supérieure
d'Électrotechnique
d'Électronique
d'Informatique
d'Hydraulique
et des
Télécommunications

In brief

› **Code:** N8EN16

Presentation

Objectives

The aim of this course is to present the main digital processors in use in the telecommunication industry. A first part will be dedicated to the study of FPGA processors and their programming using the VHDL language.

The second part will be dedicated to specific processors like DSP or GPU processors, while introducing algorithm design methodologies for hardware implementation.